30V 9mohm P-channel Trench MOSFET
AKT30P40Q

Description:

This P channel Trench MOSFET has been designed to very low on-state resistance (R_{DSON}), suggested use for Load Switch, Power management and general-purpose applications.

Features:

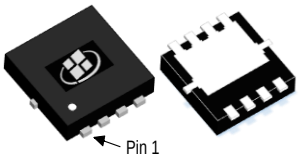
- Low $R_{DS(ON)}$
- RoHS compliant
- Halogen-free

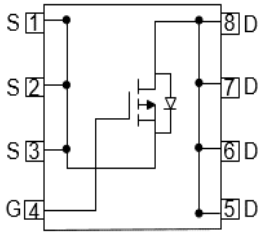
Applications:

- Load Switch
- Power Management
- DC-DC Converter

Key Performance Parameters:

Parameter	Value	Unit
V_{DS}	-30	V
$R_{DSON,\ max}$ @V_{GS} = -10 V	9	mΩ
I_D	-36	A





Ordering Information:

Ordering Code	Package Type	Marking Code	Form	Packing
AKT30P40Q	PDFN3X3	AKT30P40Q	Tape Reel	5000 per reel

Maximum Ratings (T_A = 25°C unless otherwise noted)

Symbol	Parameter	Value	Units
V_{DS}	Drain - Source Voltage	-30	V
I_D	Drain Current - Continuous (T_C = 25°C) (Note 1)	-48	A
	Drain Current - Continuous (T_C = 25°C) (Note 2)	-36	A
	Drain Current - Continuous (T_C = 100°C)	-30	A
I_{DM}	Drain Current - Pulsed (Note 3)	-144	A
V_{GS}	Gate - Source Voltage	±20	V
E_{AS}	Single Pulsed Avalanche Energy (Note 4)	121	mJ
P_D	Power Dissipation (T_C = 25°C)	32	W
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150	°C

Thermal Characteristics

Symbol	Parameter	Value	Units
$R_{\theta JC}$	Thermal Resistance, Junction - to - Case, Steady-State	3.9	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction - to - Ambient, Steady State (Note 5)	60	°C/W

Notes:

1. The max drain current rating is silicon limited
2 The max drain current rating is package limited
3. Repetitive Rating: Pulse width limited by maximum junction temperature
4. L = 0.5 mH, V_{DD} = -20 V, I_{AS} = -22 A, R_g = 50 Ω, Starting T_J = 25 °C
5. Mount on minimum PCB layout

Electrical Characteristics (T_J = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Static Characteristics						
$V_{(BR)DSS}$	Drain - Source Breakdown Voltage	V_{GS} = 0 V, I_D = -250 μA	-30			V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = -30 V, V_{GS} = 0 V,			-1	μA
I_{GSS}	Gate Leakage Current	V_{GS} = ±20 V, V_{DS} = 0 V			±100	nA
$V_{GS(th)}$	Gate Threshold voltage	V_{DS} = V_{GS}, I_D = -250 μA	-1	-1.6	-2.5	V
$R_{DS(ON)}$	Drain - Source on - state resistance	V_{GS} = -10 V, I_D = -20 A		7	9	mΩ
	Drain - Source on - state resistance	V_{GS} = -4.5 V, I_D = -15 A		9.7	13	mΩ
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{DS} = -15 V, V_{GS} = 0 V, f = 1 MHz		3310		pF
C_{oss}	Output Capacitance			395		pF
C_{rss}	Reverse Transfer Capacitance			362		pF
R_g	Gate Resistance	f = 1 MHz		6		Ω
Switching Characteristics						
$t_{d(on)}$	Turn On Delay Time	V_{DD} = -15 V , I_D = -20 A, V_{GS} = -10 V, R_G = 2.7 Ω		9		ns
t_r	Rise Time			50		ns
$t_{d(off)}$	Turn Off Delay Time			90		ns
t_f	Fall Time			70		ns
Q_g	Total Gate Charge	V_{DD} = -15 V , I_D = -20 A, V_{GS} = -10 V		64		nC
Q_{gs}	Gate-Source Charge			8.5		nC
Q_{gd}	Gate-Drain Charge			13		nC
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Body-Diode Forward Current				-36	A
I_{SM}	Maximum Pulsed Body-Diode Forward Current				-144	A
V_{SD}	Diode Forward Voltage	V_{GS} = 0 V, I_S = -20 A		-0.87		V
t_{rr}	Reverse recovery time	V_{DD} = -20 V, I_D = -20 A, di/dt = 100 A/μs		38		ns
Q_{rr}	Reverse recovery charge			20		nC
I_{rrm}	Peak Reverse Recovery Current			-1		A

Electrical Characteristics Diagrams

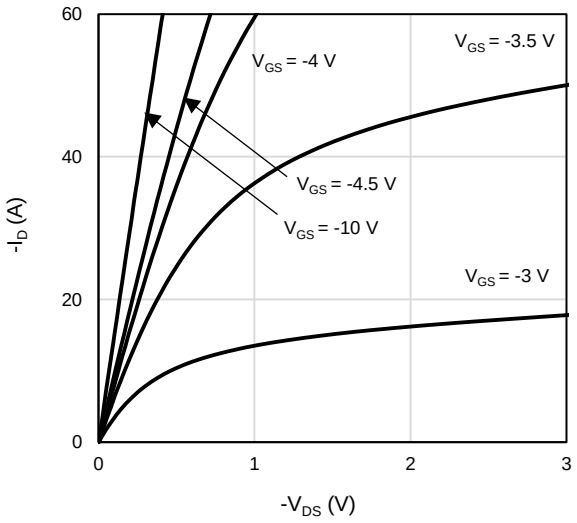


Figure 1: On-Region Characteristics

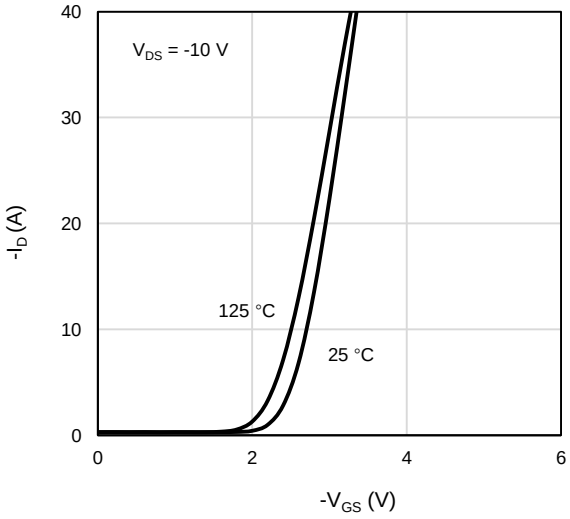


Figure 2: Transfer Characteristics

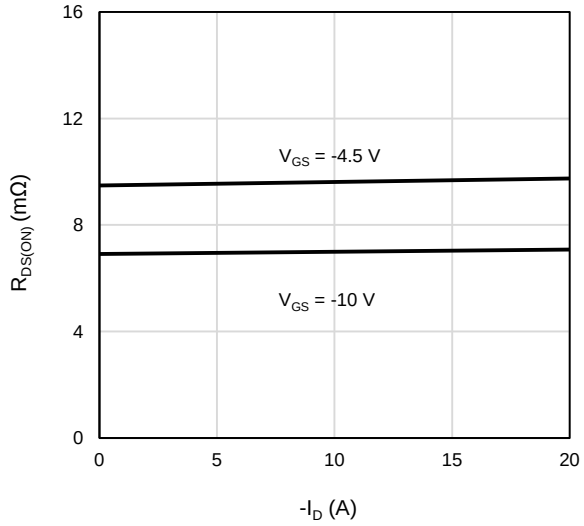


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

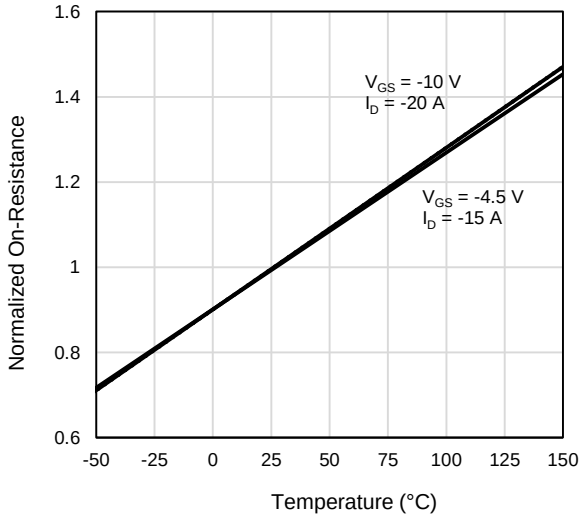


Figure 4: On-Resistance vs. Junction Temperature

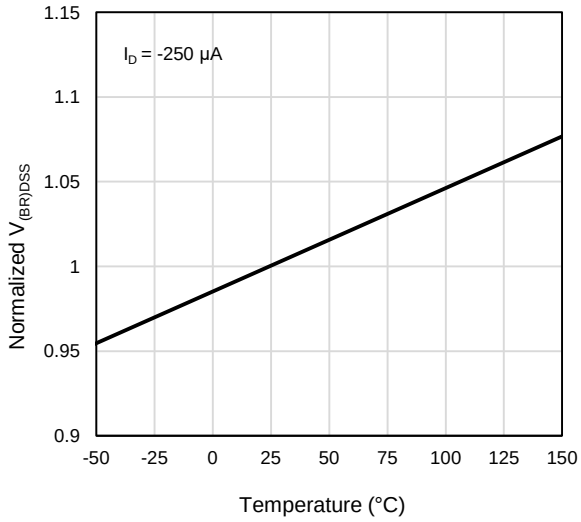


Figure 5: Breakdown Voltage vs. Junction Temperature

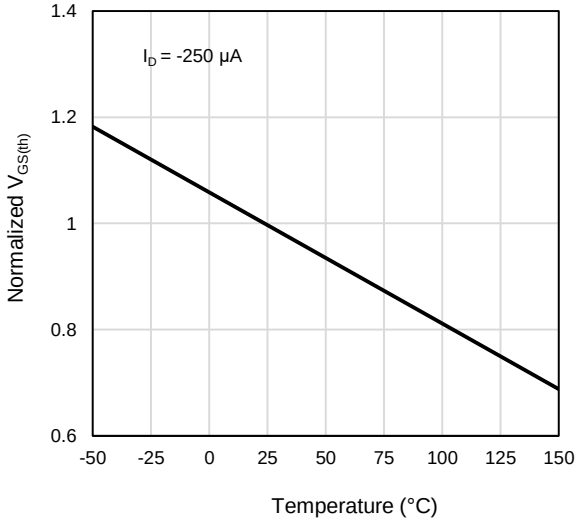


Figure 6: Threshold Voltage vs. Junction Temperature

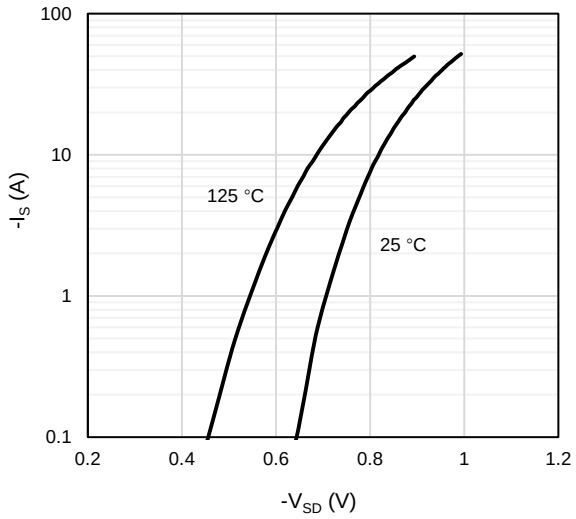


Figure 7: Body-Diode Characteristics

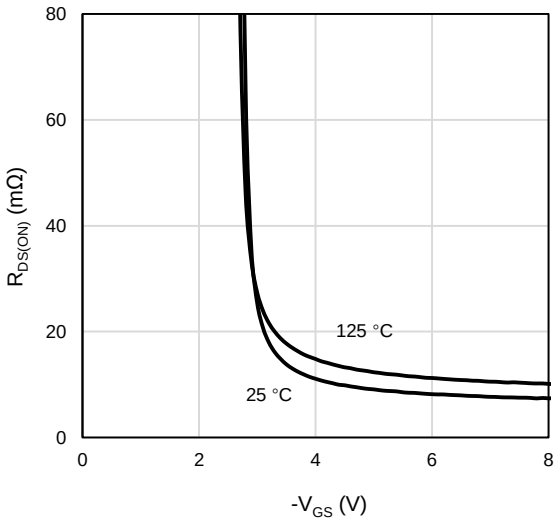


Figure 8: On-Resistance vs. Gate-Source Voltage

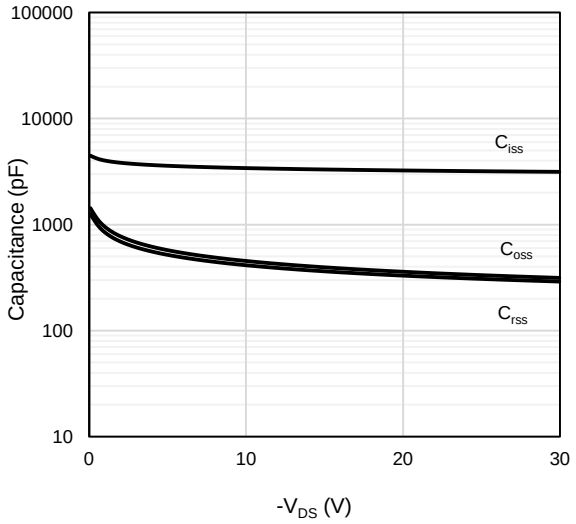


Figure 9: Capacitance Characteristics

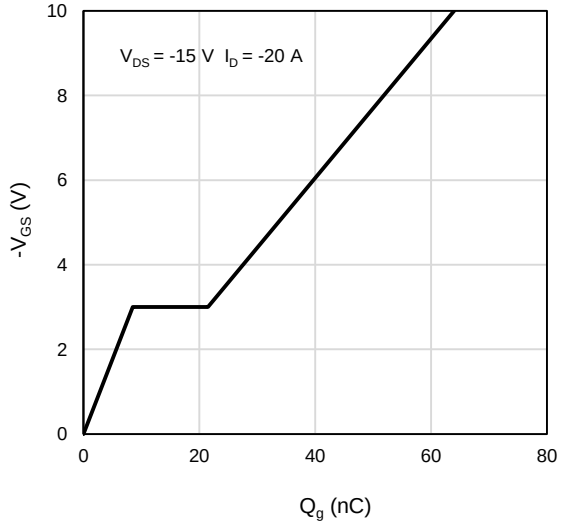


Figure 10: Gate-Charge Characteristics

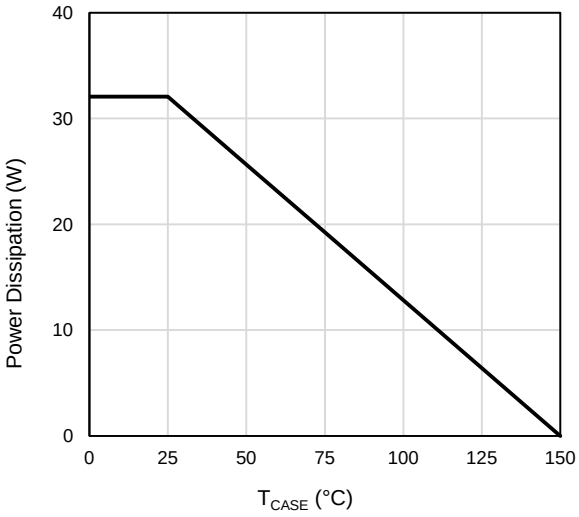


Figure 11: Power De-rating

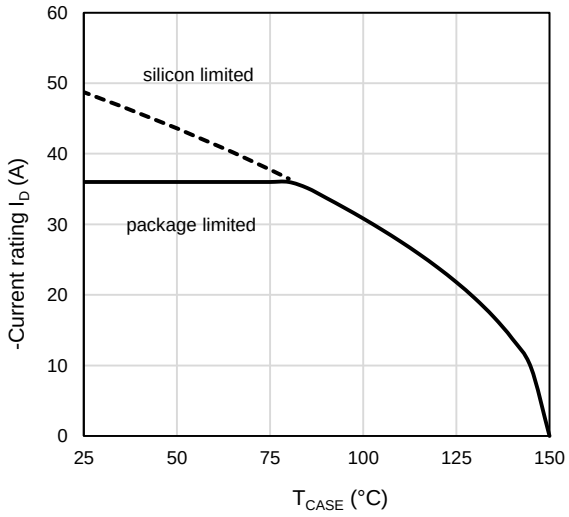


Figure 12: Current De-rating

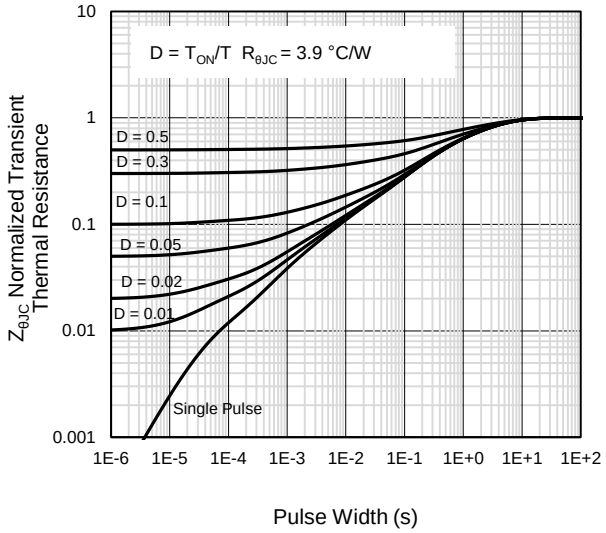


Figure 13: Normalized Maximum Transient Thermal Impedance

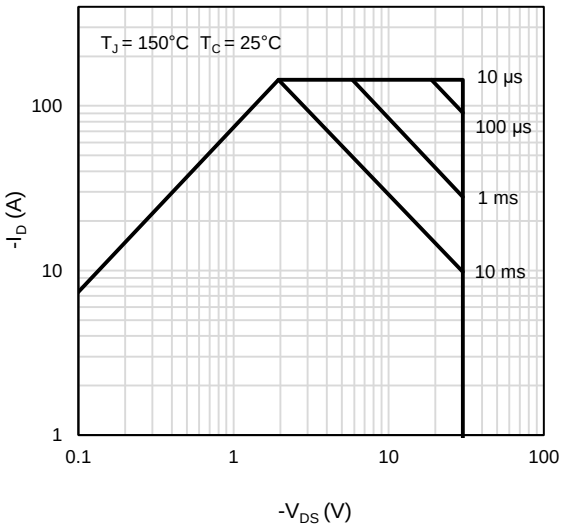
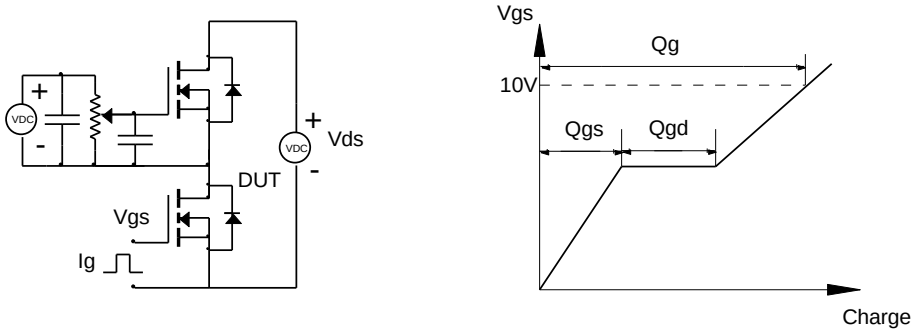


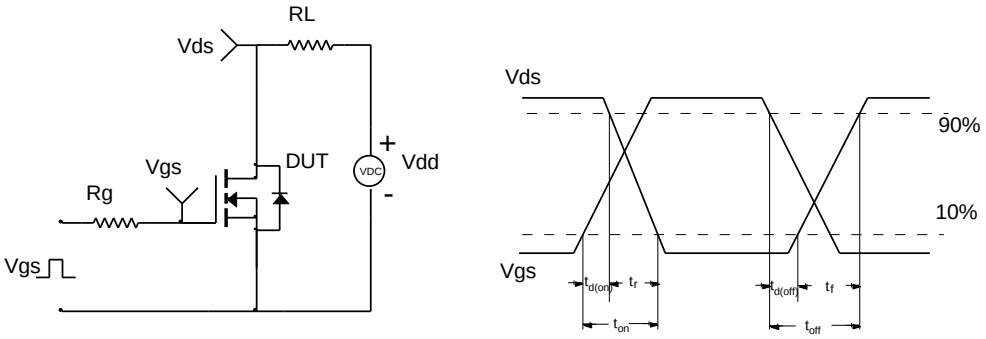
Figure 14: Maximum Forward Biased Safe Operating Area

Test Circuit and Waveform

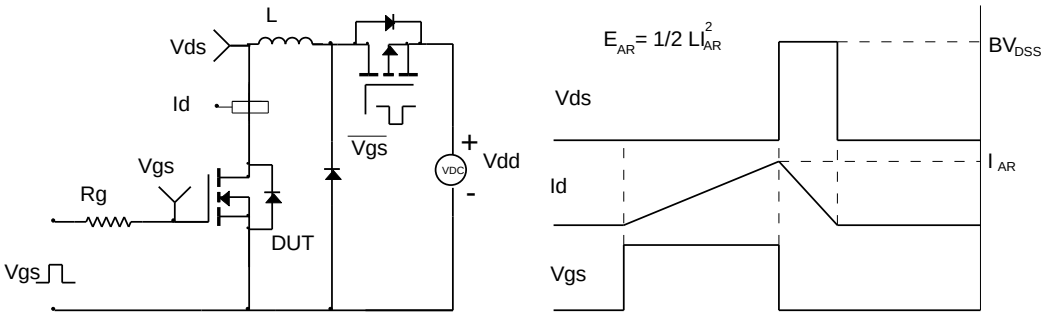
Gate Charge Test Circuit & Waveform



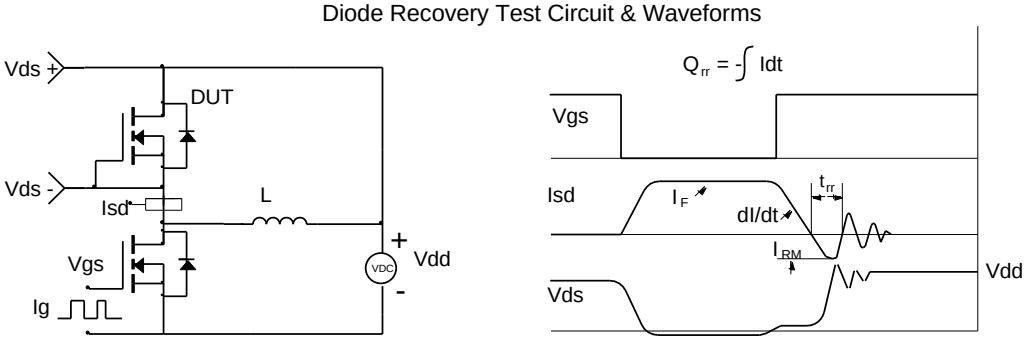
Resistive Switching Test Circuit & Waveforms



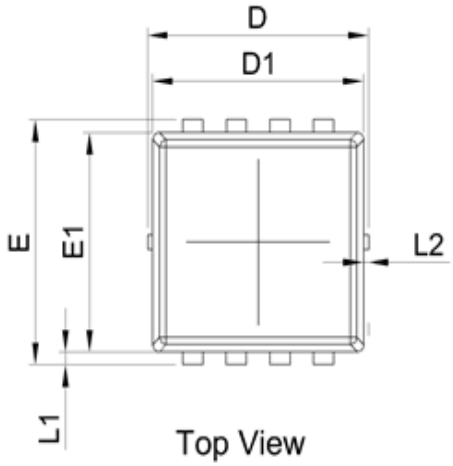
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



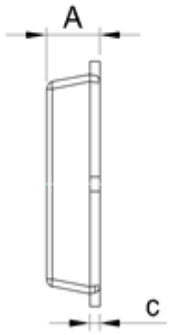
Diode Recovery Test Circuit & Waveforms



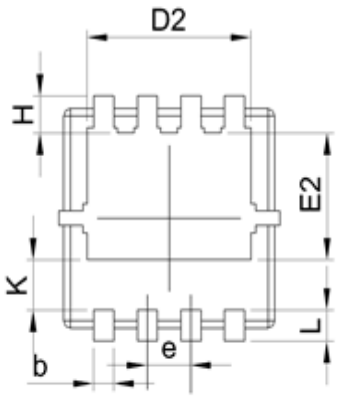
Package Outlines



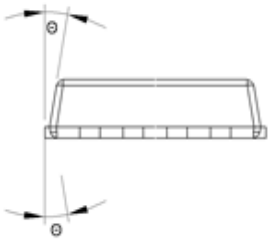
Top View



Side View



Bottom View

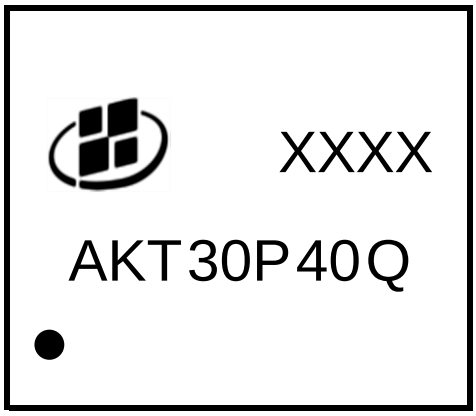


Side View

SYMBOL	MIN	NOM	MAX
A	0.70	0.80	0.90
b	0.20	0.30	0.40
c	0.14	0.15	0.25
D	3.20	3.30	3.40
D1	3.00	3.15	3.30
D2	2.35	2.45	2.55
e	0.65 BSC		
E	3.25	3.35	3.45
E1	2.85	3.00	3.15
E2	1.635	1.735	1.835
H	0.41	0.56	0.71
K	0.585	0.685	0.785
L	0.30	0.40	0.50
L1	0.05	0.15	0.25
L2	–	–	0.15
θ	8°	10°	12°

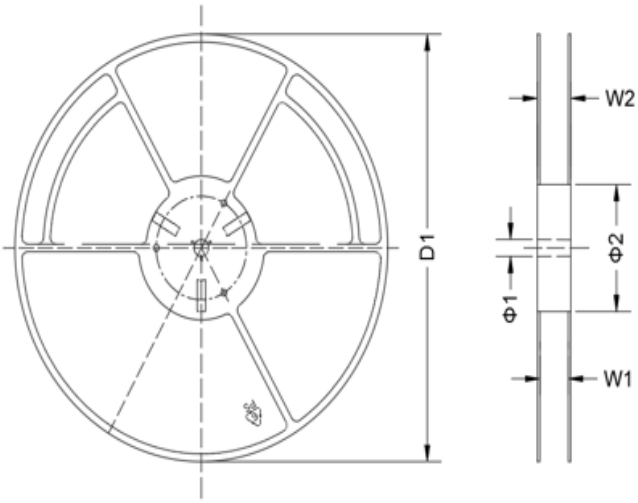
Unit in mm

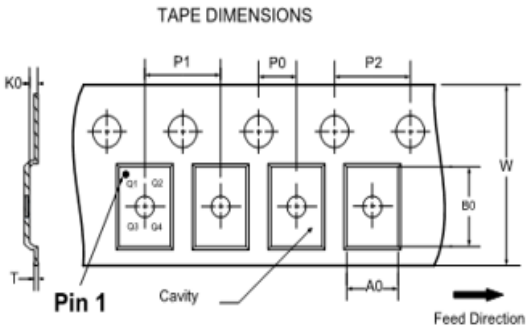
Marking Information



Note:
AKT30P40Q = Product Name Code
XXXX = Date code
Contact ALKAIDSEMI sales for detail information

Tape & Reel Information





A0：Dimension designed to accommodate the component width
B0：Dimension designed to accommodate the component length
K0：Dimension designed to accommodate the component thickness
W：Overall width of the carrier tape
P0：Pitch between successive cavity centers and sprocket hole
P1：Pitch between successive cavity centers
P2：Pitch between sprocket hole
T：Tape material thickness
D1：Reel Diameter
W1：Reel Width

DIMENSIONS(Unit:mm)										
	D1	W1	W2	Φ1	Φ2					Material
Reel	330	12.4	18.4	13.5	100					Hips
	P0	P1	P2	W	A0	B0	K0	T	Pin 1 Quadrant	Material
Tape	2	8	4	12	3.6	3.6	1.2	0.25	Q1	PC
All dimensions are nominal										

Revision History

Revision	Released	Remark
Rev.1.1	2024	

Disclaimer

The information given in this document describes the independent performance of the product, but similar performance is not guaranteed under other working conditions, and cannot be guaranteed when installed with other products or equipment. To achieve the required performance of the product in actual scenarios, the customer should conduct a complete application test to assess the functionality of the product.

Alkaidsemi assumes no responsibility for equipment failures result from using products at values that exceed the ratings, operating conditions, or other parameters listed in the product specifications.

The product described in this specification is not applicable for aerospace or other applications which requires high reliability. Customers using or selling these products for use in medical, life-saving, or life-sustaining applications do so at their own risk and agree to fully indemnify.

Due to product or technical improvements, the information described or contained herein may be changed without prior notice.