

## 40V 6mohm N-channel Trench MOSFET AKT4055G

### Description:

This N channel Trench MOSFET has been designed to very low on-state resistance ( $R_{DS(ON)}$ ), suggested use for Load Switch, PWM application Power management and general-purpose applications.

### Features:

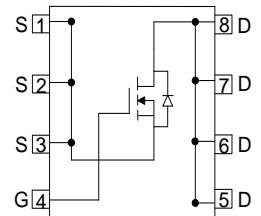
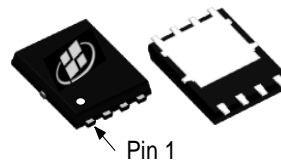
- Low  $R_{DS(ON)}$
- RoHS compliant
- Halogen-free

### Applications:

- Load Switch
- Power Management
- DC-DC Converter

### Key Performance Parameters:

Parameter	Value	Unit
$V_{DS}$	40	V
$R_{DS(ON), max} @ V_{GS} = 10 V$	6	m $\Omega$
$I_D$	54	A



### Ordering Information:

Ordering Code	Package Type	Marking Code	Form	Packing
AKT4055G	PDFN5X6	AKT4055G	Tape Reel	5000 per reel

## Maximum Ratings ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Value	Units
$V_{DS}$	Drain - Source Voltage	40	V
$I_D$	Drain Current - Continuous ( $T_C = 25^\circ\text{C}$ ) <sup>(Note 1)</sup>	67	A
	Drain Current - Continuous ( $T_C = 25^\circ\text{C}$ ) <sup>(Note 2)</sup>	54	A
	Drain Current - Continuous ( $T_C = 100^\circ\text{C}$ )	42	A
$I_{DM}$	Drain Current - Pulsed <sup>(Note 3)</sup>	216	A
$V_{GS}$	Gate - Source Voltage	$\pm 20$	V
$E_{AS}$	Single Pulsed Avalanche Energy <sup>(Note 4)</sup>	81	mJ
$P_D$	Power Dissipation ( $T_C = 25^\circ\text{C}$ )	52	W
$T_J, T_{STG}$	Operating and Storage Temperature Range	-55 to +150	$^\circ\text{C}$

## Thermal Characteristics

Symbol	Parameter	Value	Units
$R_{\theta JC}$	Thermal Resistance, Junction - to - Case, Steady State	2.4	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction - to - Ambient, Steady State <sup>(Note 5)</sup>	50	$^\circ\text{C}/\text{W}$

### Notes:

1. The max drain current rating is silicon limited
2. The max drain current rating is package limited
3. Repetitive Rating: Pulse width limited by maximum junction temperature
4.  $L = 0.5 \text{ mH}$ ,  $V_{DD} = 25 \text{ V}$ ,  $I_{AS} = 18 \text{ A}$ ,  $R_g = 25 \Omega$ , Starting  $T_J = 25^\circ\text{C}$
5. Mount on minimum PCB layout

<b>Electrical Characteristics</b> ( $T_J = 25^\circ\text{C}$ unless otherwise noted)						
Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
<b>Static Characteristics</b>						
$V_{(BR)DSS}$	Drain - Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 250\ \mu\text{A}$	40			V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 40\text{ V}, V_{GS} = 0\text{ V},$			1	$\mu\text{A}$
$I_{GSS}$	Gate Leakage Current	$V_{GS} = \pm 20\text{ V}, V_{DS} = 0\text{ V}$			$\pm 100$	nA
$V_{GS(th)}$	Gate Threshold voltage	$V_{DS} = V_{GS}, I_D = 250\ \mu\text{A}$	1	1.4	2.5	V
$R_{DS(ON)}$	Drain - Source on - state resistance	$V_{GS} = 10\text{ V}, I_D = 30\text{ A}$		4.7	6	$\text{m}\Omega$
	Drain - Source on - state resistance	$V_{GS} = 4.5\text{ V}, I_D = 20\text{ A}$		6.2	9	$\text{m}\Omega$
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS} = 20\text{ V}, V_{GS} = 0\text{ V},$ $f = 1\text{ MHz}$		2900		pF
$C_{oss}$	Output Capacitance			213		pF
$C_{rss}$	Reverse Transfer Capacitance			183		pF
$R_g$	Gate Resistance	$f = 1\text{ MHz}$		3.2		$\Omega$
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn On Delay Time	$V_{DD} = 20\text{ V}, I_D = 20\text{ A},$ $V_{GS} = 10\text{ V}, R_G = 3.3\ \Omega$		8		ns
$t_r$	Rise Time			52		ns
$t_{d(off)}$	Turn Off Delay Time			73		ns
$t_f$	Fall Time			83		ns
$Q_g$	Total Gate Charge	$V_{DD} = 20\text{ V}, I_D = 30\text{ A},$ $V_{GS} = 10\text{ V}$		56		nC
$Q_{gs}$	Gate - Source Charge			9		nC
$Q_{gd}$	Gate - Drain Charge			14		nC
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_S$	Maximum Continuous Body - Diode Forward Current				54	A
$I_{SM}$	Maximum Pulsed Body - Diode Forward Current				216	A
$V_{SD}$	Diode Forward Voltage	$V_{GS} = 0\text{ V}, I_S = 30\text{ A}$		0.84		V
$t_{rr}$	Reverse recovery time	$V_{DD} = 20\text{ V}, I_D = 20\text{ A},$ $di/dt = 100\text{ A}/\mu\text{s}$		21		ns
$Q_{rr}$	Reverse recovery charge			15		nC
$I_{rrm}$	Peak Reverse Recovery Current			1.3		A

# Electrical Characteristics Diagrams

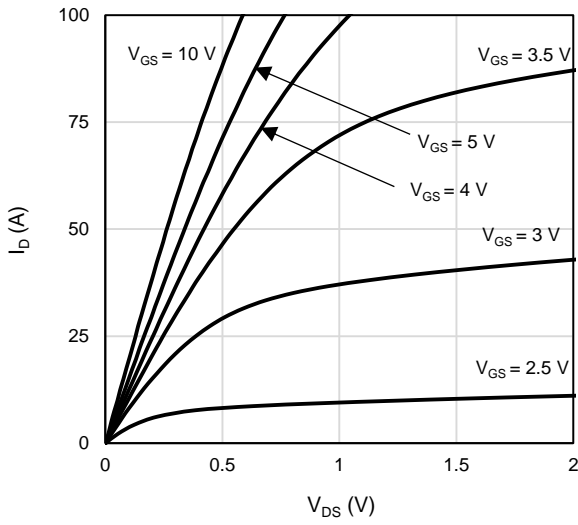


Figure 1: On-Region Characteristics

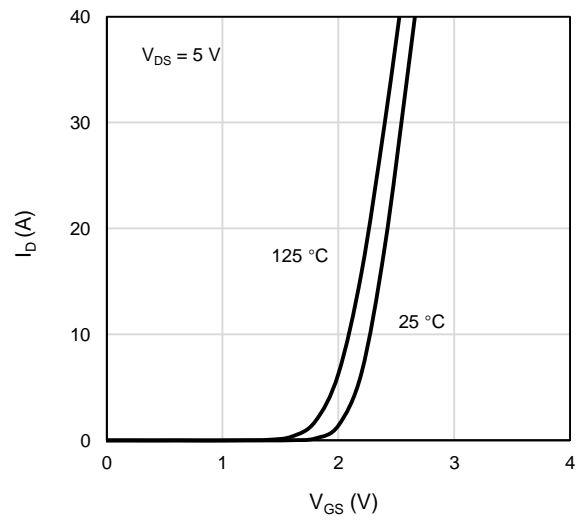


Figure 2: Transfer Characteristics

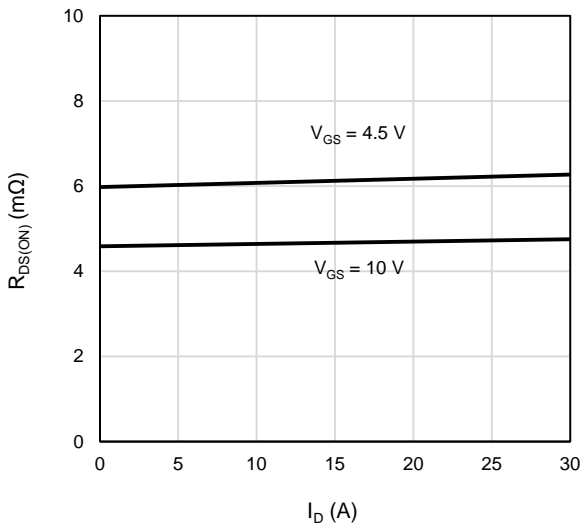


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

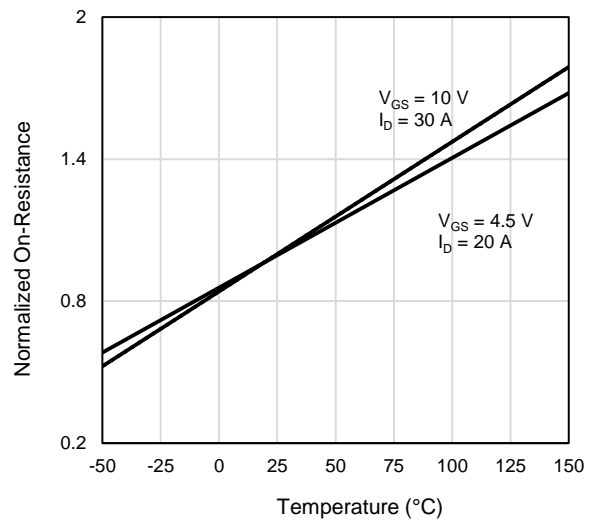


Figure 4: On-Resistance vs. Junction Temperature

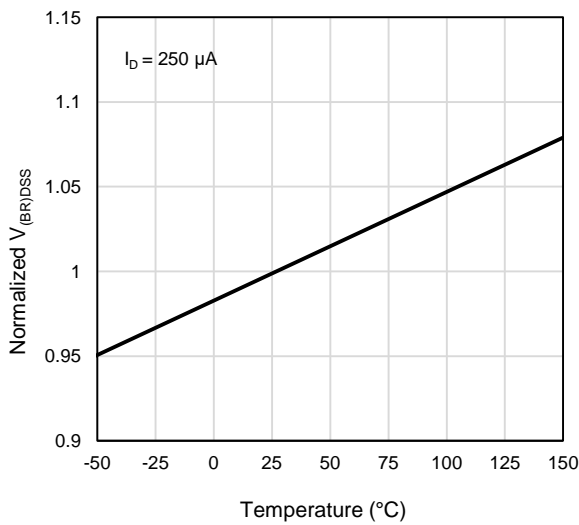


Figure 5: Breakdown Voltage vs. Junction Temperature

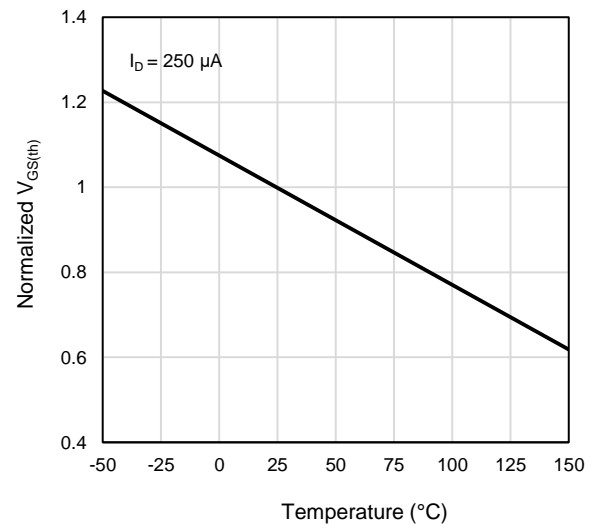


Figure 6: Threshold Voltage vs. Junction Temperature

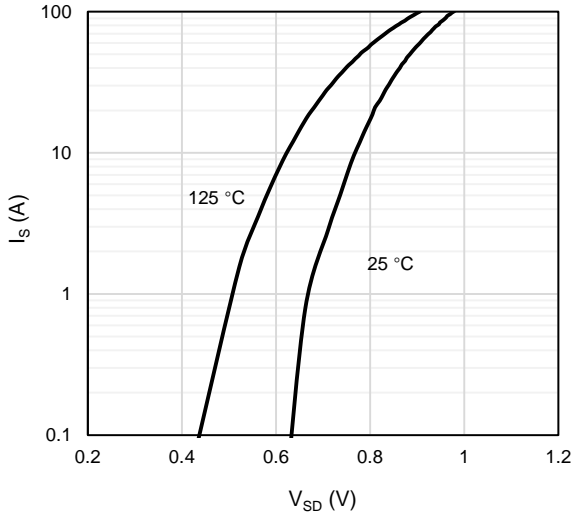


Figure 7: Body-Diode Characteristics

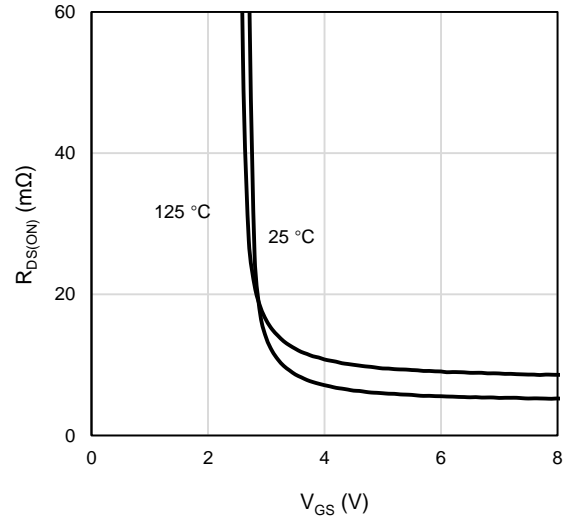


Figure 8: On-Resistance vs. Gate-Source Voltage

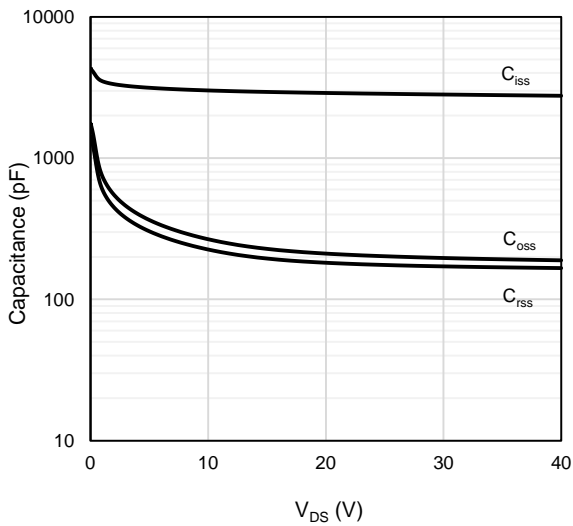


Figure 9: Capacitance Characteristics

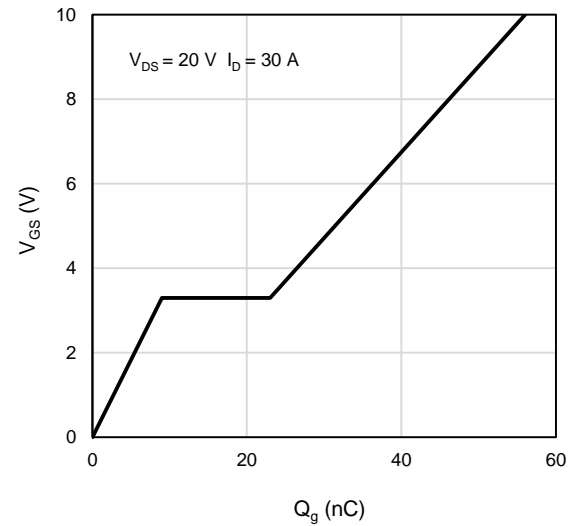


Figure 10: Gate-Charge Characteristics

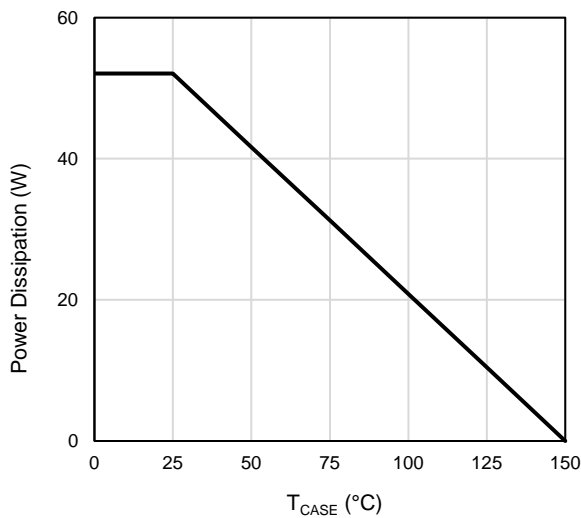


Figure 11: Power De-rating

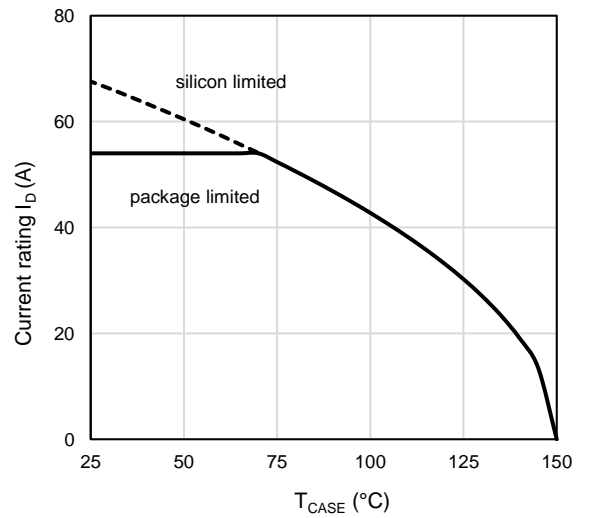


Figure 12: Current De-rating

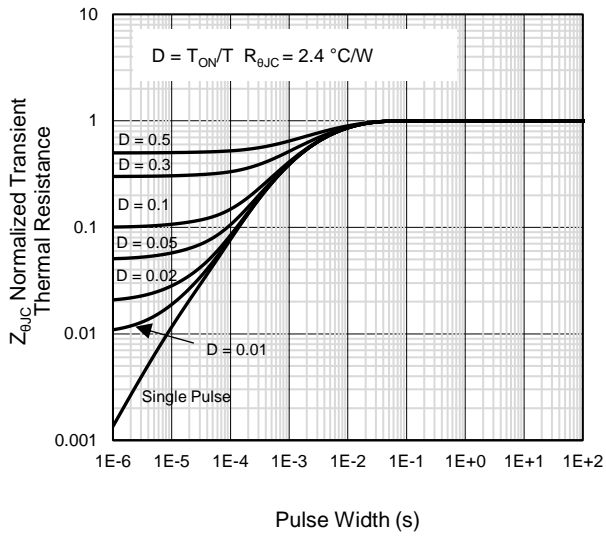


Figure 13: Normalized Maximum Transient Thermal Impedance

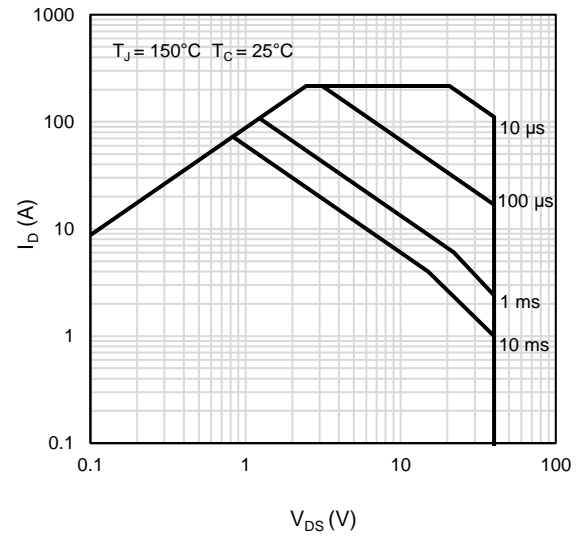
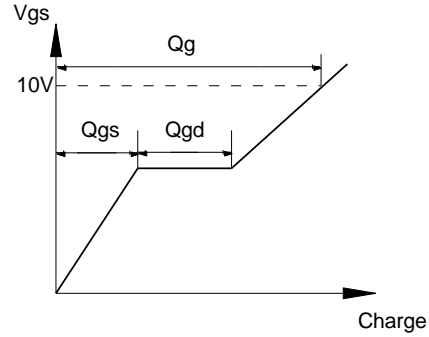
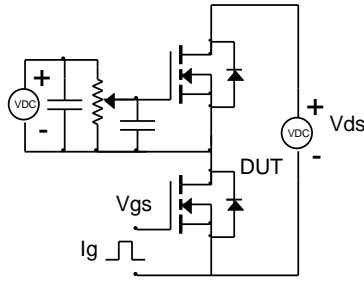


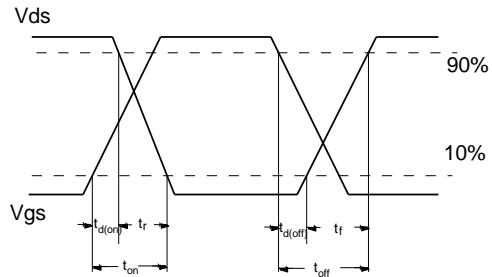
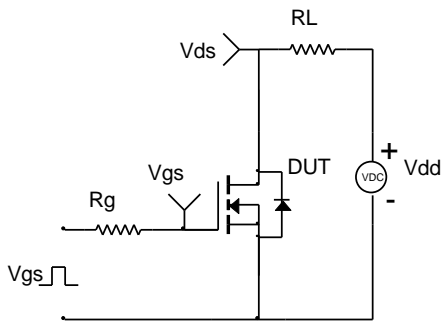
Figure 14: Maximum Forward Biased Safe Operating Area

# Test Circuit and Waveform

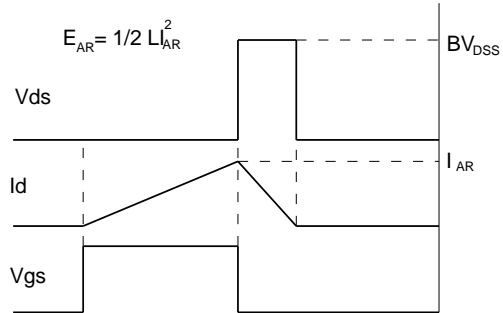
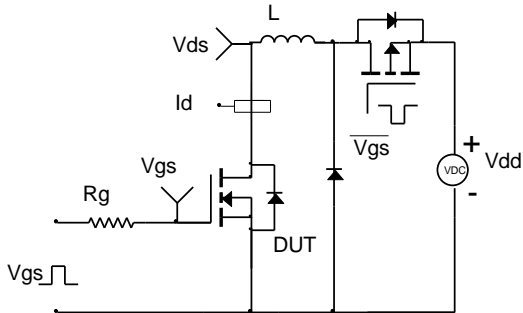
Gate Charge Test Circuit & Waveform



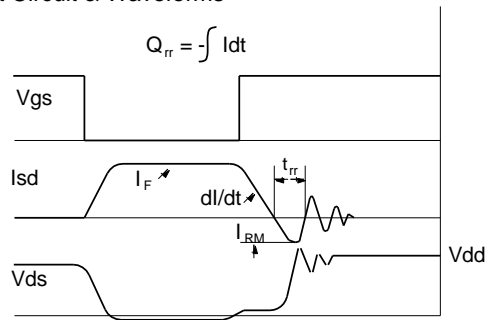
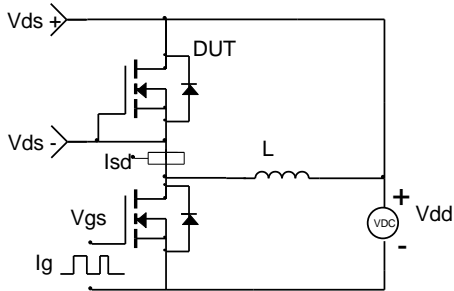
Resistive Switching Test Circuit & Waveforms



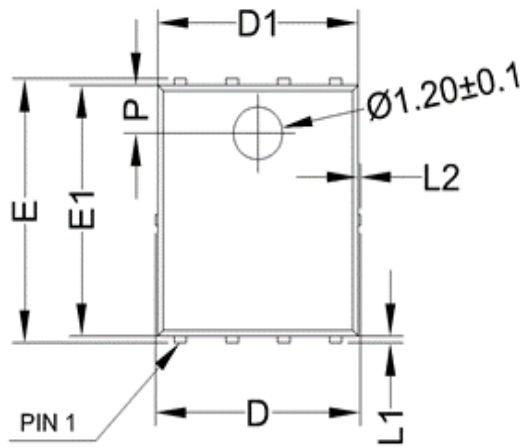
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



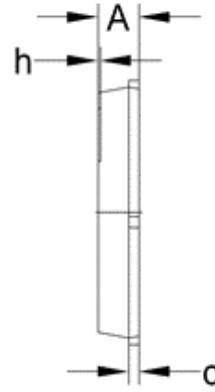
Diode Recovery Test Circuit & Waveforms



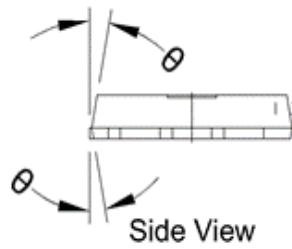
Package Outlines



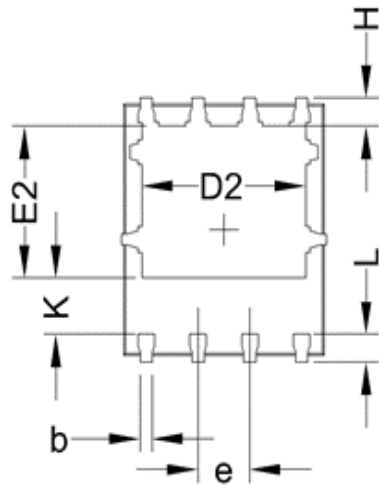
Top View



Side View



Side View



Bottom View

	MIN	NOM	MAX
A	0.90	1.00	1.10
b	0.20	0.30	0.40
c	0.21	0.25	0.34
D	-	-	5.10
D1	4.80	4.90	5.00
D2	3.91	4.01	4.11
e	1.27 BSC		
E	5.90	6.00	6.10
E1	5.65	5.75	5.85
E2	3.375	3.475	3.575
H	0.55	0.65	0.75
h	-	-	0.10
K	1.20	-	-
L	0.55	0.65	0.75
L1	0.05	0.15	0.25
L2	-	-	0.12
Ø	8°	10°	12°
P	1.00	1.10	1.20

Unit in mm



## Marking Information



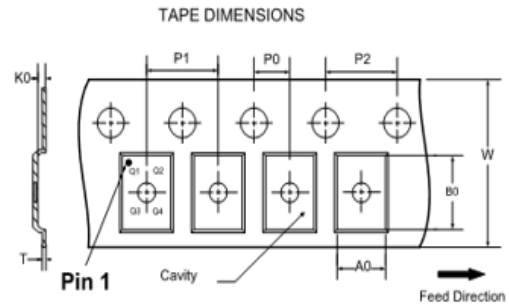
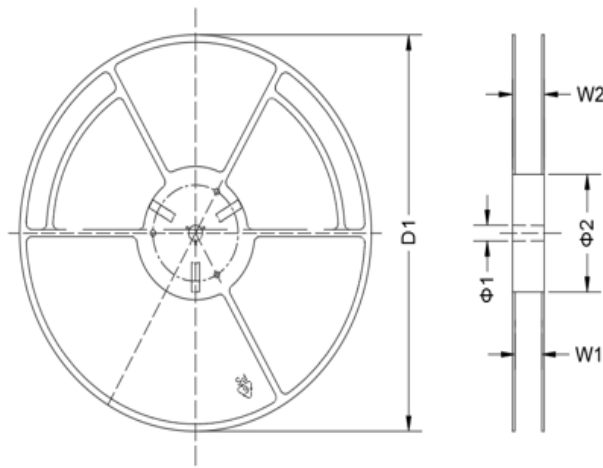
**Note:**

AKT4055G = Product Name Code

XXXX = Date code

Contact ALKAIDSEMI sales for detail information

## Tape & Reel Information



- A0: Dimension designed to accommodate the component width
- B0: Dimension designed to accommodate the component length
- K0: Dimension designed to accommodate the component thickness
- W: Overall width of the carrier tape
- P0: Pitch between successive cavity centers and sprocket hole
- P1: Pitch between successive cavity centers
- P2: Pitch between sprocket hole
- T: Tape material thickness
- D1: Reel Diameter
- W1: Reel Width

DIMENSIONS(Unit:mm)										
Reel	D1	W1	W2	φ1	φ2					Material
	330	12.4	18.4	13.5	100					Hips
Tape	P0	P1	P2	W	A0	B0	K0	T	Pin 1 Quadrant	Material
	2	8	4	12	6.3	5.3	1.2	0.25	Q1	PC

All dimensions are nominal

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**Revision History**

Revision	Released	Remark
Rev.1.1	2024	

**Disclaimer**

The information given in this document describes the independent performance of the product, but similar performance is not guaranteed under other working conditions, and cannot be guaranteed when installed with other products or equipment. To achieve the required performance of the product in actual scenarios, the customer should conduct a complete application test to assess the functionality of the product.

Alkaidsemi assumes no responsibility for equipment failures result from using products at values that exceed the ratings, operating conditions, or other parameters listed in the product specifications.

The product described in this specification is not applicable for aerospace or other applications which requires high reliability. Customers using or selling these products for use in medical, life-saving, or life-sustaining applications do so at their own risk and agree to fully indemnify.

Due to product or technical improvements, the information described or contained herein may be changed without prior notice.